FIG. 30A *B* summarizes the paths of FIG. 24 to generate the guide;

FIG. 31 depicts the progression of input/output addresses through the network of

FIG. 24;

FIG. 32A depicts an exemplary connection request constraint compliant with the

5   compressor constraint for a 5×5 switch;

FIG. 32B depicts are ordering of output addresses of the switch of FIG. 32A

which is order preserving;

FIG. 32C depicts five concurrent connections over a compressor implemented

from a generic switch;

10   FIG. 32D is a representation whereby the compressor of FIG. 32 C is bent into a

cylinder to visualize the order-preservation of the compressor;

FIGS. 33A-D shows the six combinations of concurrent connections required for

a 3×3 switch to quality as a compressor;

FIG. 34 depicts, for a generic switch, multicast connections from five input ports

15   to nine output ports that can be concurrently accommodated by an expander which are

compliant with the expander constraint;

FIGS. 35A-P depict a 4×4 switch which qualifies as a compressor if and only if

it accommodates at least the sixteen combinations of concurrent point-to-point connections